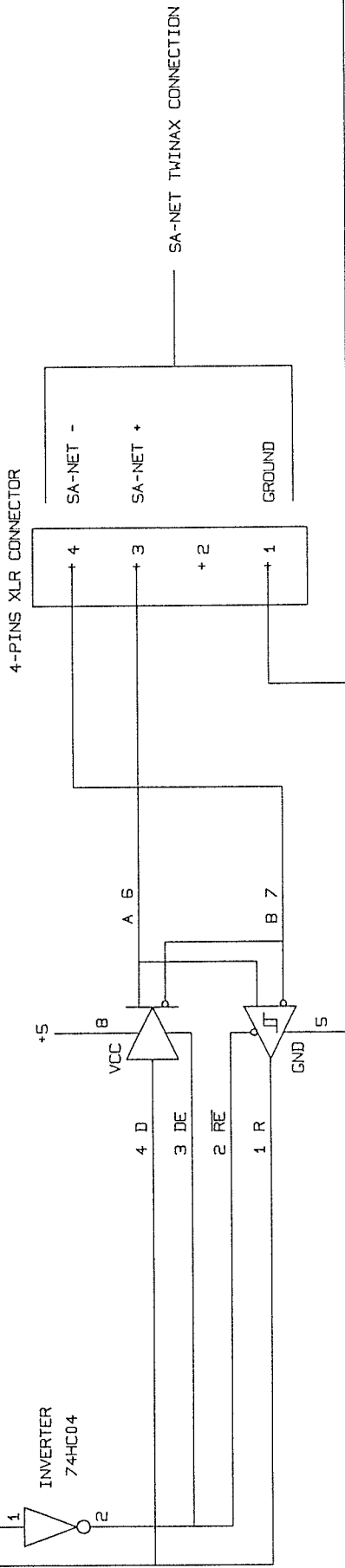
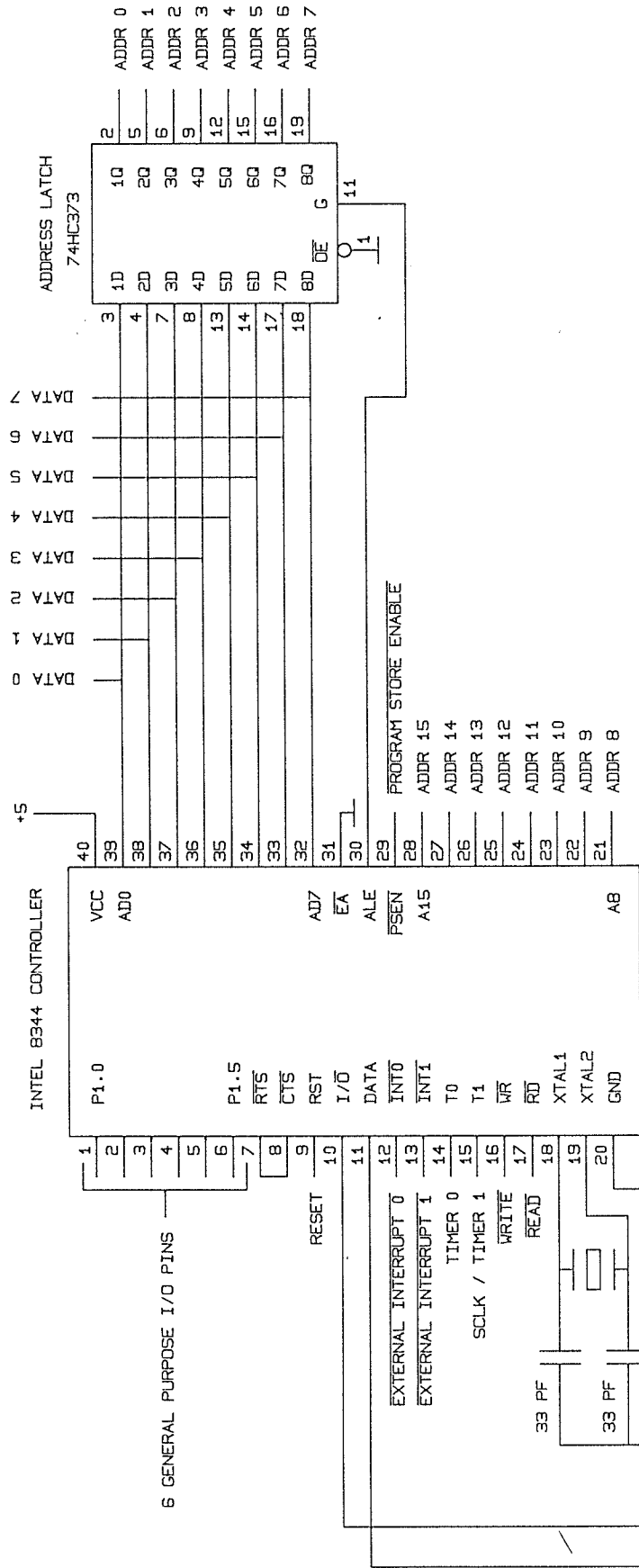


\*\*\* SECONDARY STATION MEMORY MAP \*\*\*

Hex Address	Description
00 - 02	reset vector
03 - 05	external 0 interrupt service routine vector
06	not used
07	not used
08	not used
09	not used
0A	not used
0B - 0D	timer 0 interrupt service routine vector
0E	not used
0F	not used
10	not used
11	not used
12	not used
13 - 15	external 1 interrupt service routine vector
16	normal mode: not used                      boot mode: device type
17	normal mode: not used                      boot mode: identification code 0
18	normal mode: not used                      boot mode: identification code 1
19	normal mode: not used                      boot mode: identification code 2
1A	not used
1B - 1D	timer 1 interrupt service routine vector
1E	not used
1F	not used
20	not used
21	not used
22	not used
23 - 25	serial (SIU) interrupt service routine vector
26	EEPROM program checksum (MSB)
27	EEPROM program checksum (LSB)
28 - 29	switch bank routine:            CLR            EA
2A - 2C	MOV            DPTR,#SW_ADDR (6000)
2D - 2D	MOVX          A,@DPTR
2E - 2F	SJMP          RESET
30 - 7F	reserved for modifiable system constants
80 - XXXX	normal program



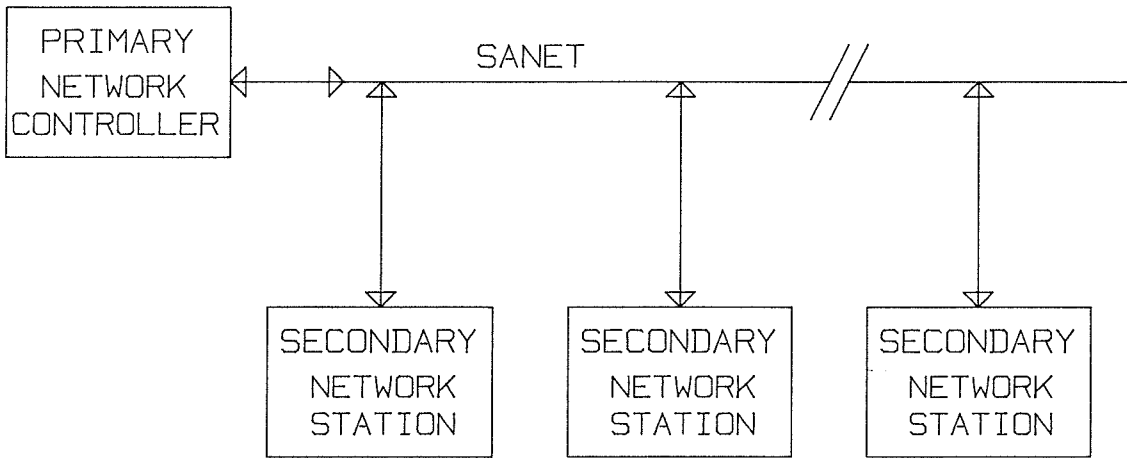


FIGURE 1. SANET MULTIPOINT CONFIGURATION.

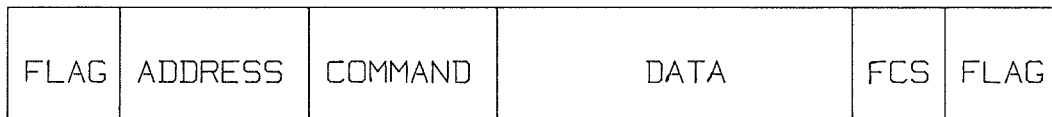


FIGURE 2. SANET SDLC FRAME

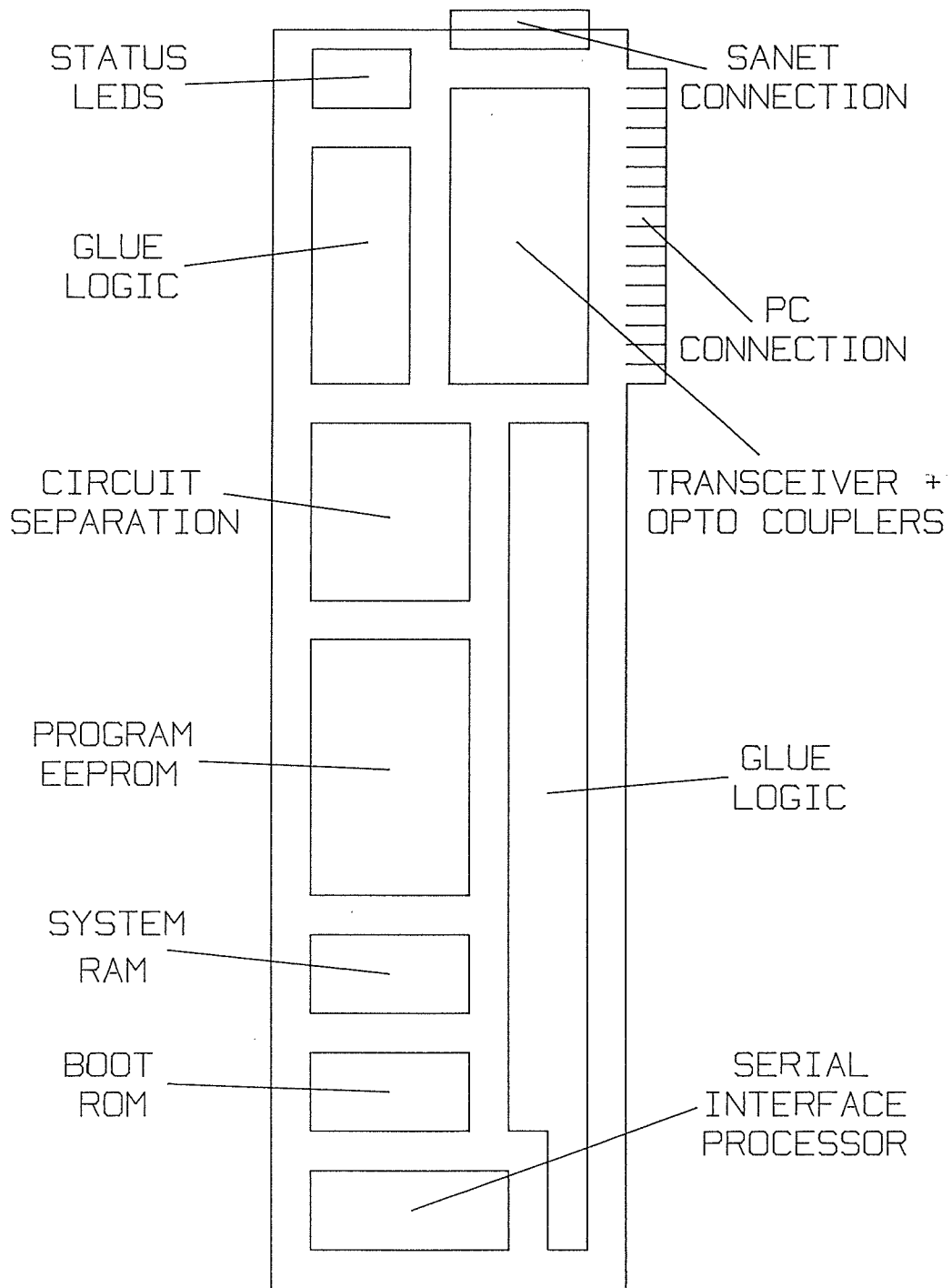


FIGURE 3. SANET CONTROLLER EXTENSION CARD.

